	Application No.	Applicant(s)	A
Notice of Allowability		, (pp.:/out(o/	(M)
	10/041,957	TEIG ET AL.	,
	Examiner	Art Unit	
	Binh C. Tat	2825	
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in t or other appropriate commun GHTS. This application is sul	his application. If not includ ication will be mailed in due	ed course. <b>THIS</b>
1. This communication is responsive to <u>12/19/05</u> .			
2. The allowed claim(s) is/are <u>27-47</u> .			
<ul> <li>3. ☐ Acknowledgment is made of a claim for foreign priority uner a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents have</li> </ul>		(f).	
2. Certified copies of the priority documents have been received in Application No			
3. Copies of the certified copies of the priority documents have been received in this national stage application from the			
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" on noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		reply complying with the re-	quirements
4. A SUBSTITUTE OATH OR DECLARATION must be submi INFORMAL PATENT APPLICATION (PTO-152) which give	tted. Note the attached EXANes reason(s) why the oath or d	INER'S AMENDMENT or Neclaration is deficient.	IOTICE OF
5. CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.			
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached			
1)  hereto or 2)  to Paper No./Mail Date			
(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	84(c)) should be written on the se header according to 37 CFR	drawings in the front (not the 1.121(d).	back) of
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5 🗆 Notice of Infer	rmal Potent Application (PT)	) 150\
Notice of References Cited (FTO-992)     Notice of Draftperson's Patent Drawing Review (PTO-948)		mal Patent Application (PT0	J- 102)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08	Paper No./Ma	ail Date <u>01/21/06</u> . mendment/Comment	
Paper No./Mail Date	,	atement of Reasons for Allo	wance
of Biological Material			
	9.  Other		

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## **DETAILED ACTION**

## **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Adeli Mani (Reg. No. 39585) on 01/21/06.

The application has been amended as follow:

Claim 27 line 3, change "prior to performing a routing operation, defining a set of partitioning lines for partitioning, during the routing operation, a region of an integrated circuit ("IC") layout into a plurality of sub-regions" to --prior to performing a routing operation, defining a set of partitioning lines for partitioning a region of an integrated circuit ("IC") layout into a plurality of sub-regions, during the routing operation--.

Claim 46 line 3, change "prior to performing a routing operation, defining a set of partitioning lines for partitioning, during the routing operation, a region of an integrated circuit ("IC") layout into a plurality of sub-regions" to --prior to performing a routing operation, defining a set of partitioning lines for partitioning a region of an integrated circuit ("IC") layout into a plurality of sub-regions, during the routing operation--.

Claim 47 line 3, change "defining a set of partitioning lines for partitioning, during the routing operation, a region of an integrated circuit ("IC") layout into a plurality of sub-

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regions" to --defining a set of partitioning lines for partitioning a region of an integrated circuit ("IC") layout into a plurality of sub-regions, during the routing operation--.

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## Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

- 1. Claims 27-47 are allowed because the prior art does not teach or suggest a method of pre-computing routes for nets, the method comprising: a) prior to performing a routing operation, defining a set of partitioning lines for partitioning, a region of an integrated circuit ("IC") layout into a plurality of sub-regions, during the routing operation; b) for a set of potential sub-regions, identifying a set of at least two routes that traverse the potential set of sub-regions, wherein at least one of the routes has at least one diagonal edge; storing the identified routes, wherein said stored routes are for use during the routing operation
- 2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

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## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Binh C. Tat whose telephone number is 571 272-1908. The examiner can normally be reached on 7:30 - 4:00 (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on 571 272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tat Binh
Patent Examiner

January 19, 2006